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# An Expressive Verification Framework for State/Event Systems

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## Abstract

Specification languages for concurrent software systems need to combine practical algorithmic efficiency with high expressive power and the ability to reason about both states and events. We address this question by defining a new branching-time temporal logic SE-AJ<sup>?</sup> which integrates both state-based and action-based properties. SE-AJ<sup>?</sup> is universal, i.e., preserved by the simulation relation, and thus amenable to counterexample-guided abstraction refinement. We provide a model-checking algorithm for this logic, and describe a compositional abstraction-refinement loop which exploits the natural decomposition of the concurrent system; the abstraction and refinement steps are performed over each component separately, and only the model checking step requires an explicit composition of the abstracted components. For experimental evaluation, we have integrated the presented algorithms in the software verification tool MAGIC, and determined a previously unknown race condition error in a piece of an industrial robot control software.





## 1 Introduction

The practical effectiveness of model checking is characterized by a trade-off between the expressive power of the specification formalism and the complexity of the corresponding model checking algorithm. For software verification, this problem is even more acute, since software is harder to specify, and state explosion is exacerbated by the concurrent execution of multiple components. The expressive power of temporal logics such as CTL or LTL is quite limited when it comes to specifying, e.g., the periodicity of events. The last decade has seen several attempts at extending the expressiveness of temporal logics [8, 32, 30, 31, 29, 13]. Recently, Clarke et al. [11] have investigated a family of universal branching logics, called  $AQ$ , which are extensions of ACTL by sets  $i?$  of  $cj$ -regular path operators. A subtle property of  $AQ$  is the *monotonicity* of the path operators: the semantics guarantees that the extended path operators cannot be used to implicitly define negation. While this property comes for free with the standard temporal path operators, its presence is crucial for obtaining extended *universal* branching logics. Such logics are preserved by simulation, and are therefore amenable to existential abstraction [9, 11].

Another shortcoming of standard temporal logics stems from the fact that for the verification of concurrent software conducted at the source code level, one needs to specify both *state* information (program counter location, memory contents) and *communication* among components. For example, the Bluetooth L2CAP specification [14] asserts that "when an L2CAP\_ConnectRsp event is received in a W4\_L2CAP\_CONNECT\_RSP state, within one time unit, an L2CAP process may send out an L2CA\_ConnectInd event, disable the RTX timer, and move to state CONFIG." As this example shows, both states (W4\_L2CAP\_CONNECT\_RSP and CONFIG) and events (L2CAP\_ConnectRsp and L2CA\_ConnectInd) are required to properly capture the desired L2CAP behavior.

Generally, in concurrent programs, communication among modules proceeds via actions (events) which can represent function calls, requests and acknowledgments, etc. These communications can be data dependent and carry data on its channels. Existing model checking techniques typically use either *state-based* or *event-based* formalisms to represent finite-state models of programs. In principle, both frameworks are interchangeable: an action can be encoded as a change in state variables, and likewise one can equip a state with different actions to reflect different values of its internal variables. Neither approach on its own is practical, however, when it comes to the specification of data-dependent communication claims: considerable domain expertise is then required to annotate the program and to specify proper specifications in temporal logic.

In this paper, we define the specification logic SE- $Ai?$  which combines the high expressive power of  $AQ$  with the ability to specify states and events simultaneously. The hybrid state/event-based semantics of SE- $Ai?$  allows us to represent both software implementations and specifications directly without program annotations or privileged insights into program execution. Note that, for example, there is no natural generic extension of standard operators such as  $U$  (*until*) to a state/event based framework (see, e.g., [18]); SE- $Ai?$ , however, enables us to employ different variants of CTL operators for actions and data valuations simultaneously at no additional expense. Notwithstanding its high expressive power and versatility, SE- $Ai?$  lends itself naturally to an

efficient verification strategy which combines counterexample-guided abstraction refinement (CEGAR [20, 7]) and compositional reasoning: starting with a coarse initial abstraction, our CEGAR scheme computes increasingly precise abstractions of the target system by analyzing spurious counterexamples until either a real counterexample is obtained or the system is found to be correct. More precisely, given a system  $M$  composed of  $n$  concurrent components  $M_1, \dots, M_n$ , and a SE-AJ? specification  $ip$ , the verification of  $M \models ip$  proceeds as follows:

1. **Abstract.** Create an abstraction  $\widehat{M}$  such that all behaviors of  $\widehat{M}$  are preserved by  $M$ . This is done component-wise without constructing **the full** state space of  $M$ .
2. **Verify.** Verify whether  $\widehat{M} \models ip$ . If so, report success and exit. Otherwise, extract an abstract counterexample  $\widehat{C}$  that indicates in which way  $ip$  fails in  $\widehat{M}$ .
3. **Refine.** Check whether  $\widehat{C}$  gives rise to a real counterexample over  $M$ . If  $\widehat{C}$  corresponds to a genuine behavior of  $M$  then report a failure along with a fragment of each  $M_j$  that illustrates why  $M \not\models ip$ . If  $\widehat{C}$  is spurious, on the other hand, refine  $\widehat{M}$  using  $\widehat{C}$  to obtain a more precise abstraction and repeat from step 1. This refinement step, like the initial abstraction, is performed component-wise.

Of the three steps in this abstract-verify-refine process only the verification stage of our technique requires the explicit composition of a system. The other stages can be performed one component at a time. Since verification is performed only on abstractions (which are usually much smaller than the corresponding concrete systems), our verification approach is able to significantly reduce the state space explosion problem. Another key characteristic of our algorithm is that the verification step handles both states and events *directly*, i.e., without conversion into either a pure state-based or a pure event-based framework. The model checking is therefore significantly more efficient than alternative conversion-based approaches, since it has been observed that conversion can lead to a quadratic blowup in both time and space even for reachability properties [2].

Note that the universality of SE-AJ? is crucial for the correctness of our approach, and that the verification step uses automata theoretic methods to evaluate the a>regular path operators.

To the best of our knowledge, this is the first counterexample-guided, compositional abstraction refinement scheme to perform verification of branching-time specifications. We have implemented our approach in our C verification tool MAGIC [22] which extracts state/event finite-state models from C programs automatically via predicate abstraction [28, 3]. Our experiments with a piece of robot controller software resulted in the detection of a complicated race condition error.

The rest of this article is organized as follows. In Section 2 we summarize related work. This is followed by some preliminary definitions defined in Section 3. In Section 4 we present the SE-Ai? logic, followed by model checking, counterexample validation and abstraction refinement procedures described in Section 5. Finally, we give a brief overview of the application of our techniques in Section 6.

## 2 Related Work

Extensions of temporal logics to increase the expressiveness of temporal operators have been proposed by various authors [8, 32, 30, 31, 29, 13]. Wolper [32] and Vardi and Wolper [31] extended LTL by regular expressions and Büchi automata respectively. Vardi and Wolper [30] and Thomas [29] have proposed extended branching-time logics, but have not addressed model checking. Clarke et al. [8] describe the logic ECTL that similarly to our work considers  $\wedge$ -regular automata in the context of branching-time logic. However, this work does not deal with abstraction refinement or compositional methods. Clarke et al. [11] define a class  $AQ$  of universal branching logics (cf. Section 1) for a systematic study of the complexity and completeness of counterexamples in model checking. The work of [11], however, does not define a model checking algorithm for  $AQ$ . Our work extends the  $AQ$  logic with the combined state/event expressiveness and provides a model checking algorithm for SE-AJ? which also applies to  $AQ$ .

State/event-based notations have been explored by a number of authors [25, 18, 17, 2]. The novelty of our approach lies in the way in which we efficiently integrate an expressive state/event formalism with powerful state space reduction techniques, namely CEGAR and compositional reasoning. In this respect, not only do we substantially extend the expressiveness of the state/event linear temporal logic SE-LTL presented in [2], but we also show how to validate *branching (tree-like)* counterexamples in a *compositional* manner, based on new results relating simulation and weak simulation relations for parallel processes (see Theorem 4 in Section 5).

The formalization of a general notion of abstraction first appeared in [12]. The abstractions used in our approach are *conservative*. They are guaranteed to preserve 'undesirable' properties of the system (e.g., [19, 9]). Conservative abstractions usually lead to significant reductions in the state space but in general require an iterated abstraction refinement mechanism (such as CEGAR) in order to establish specification satisfaction. CEGAR has been used, among others, in [24] (in non-automated form), and [1, 26, 21, 15, 6, 10]. In particular, CEGAR-based schemes have been used for the verification of safety properties [1, 7, 15, 3] as well as liveness [2] properties.

Compositionality and abstraction have been extensively studied in process algebra (e.g., [16, 23, 27]). Abstraction and compositional reasoning have been combined [4] within a single CEGAR scheme to verify safety properties of concurrent C programs.

## 3 Preliminaries

**Definition 1 (Labeled Kripke Structure).** A labeled Kripke structure (LKS) is a 6-tuple  $(S, \text{init}, AP, C, \mathcal{L}, T)$  where (i)  $S$  is a finite non-empty set of states, (ii)  $\text{init} \in S$  is an initial state, (iii)  $AP$  is a finite set of atomic state propositions, (iv)  $C : S \rightarrow 2^{AP}$  is a state-labeling function, (v)  $E$  is a finite set of actions (alphabet) and (vi)  $T \subseteq S \times S \times S$  is a transition relation.

Given an LKS  $M = (S, \text{init}, AP, C, S, T)$ , we write  $S(M)$ ,  $\text{init}(M)$ ,  $AP(M)$ ,  $\mathcal{L}(M)$ ,  $U(M)$  and  $T(M)$  to mean  $S$ ,  $\text{init}$ ,  $AP$ ,  $\mathcal{L}$ ,  $S$  and  $T$  respectively. Given  $s, s' \in S$

$S$  and  $a \in \Sigma$  we write  $s \xrightarrow{a} s'$  to mean  $(s, a, s') \in T$ . Also, let  $Succ(s, a) = \{s' \in S \mid s \xrightarrow{a} s'\}$  and  $Enabled(s) = \{a \in \Sigma \mid Succ(s, a) \neq \emptyset\}$ . Finally, a *path* of  $M$  is an infinite sequence of consecutive transitions  $s_0 \xrightarrow{a_0} s_1 \xrightarrow{a_1} s_2 \xrightarrow{a_2} \dots$ . Note that we do not require paths to begin with *init*.

**Definition 2 (Parallel Composition).** Let  $M_1$  and  $M_2$  be two LKSs such that  $AP(M_1) \cap AP(M_2) = \emptyset$ . Then the parallel composition of  $M_1$  and  $M_2$ , denoted by  $M_1 \parallel M_2$ , is an LKS obeying the following conditions: (i)  $S(M_1 \parallel M_2) = S(M_1) \times S(M_2)$ , (ii)  $init(M_1 \parallel M_2) = (init(M_1), init(M_2))$ , (iii)  $AP(M_1 \parallel M_2) = AP(M_1) \cup AP(M_2)$ , and (iv)  $\Sigma(M_1 \parallel M_2) = \Sigma(M_1) \cup \Sigma(M_2)$ . Moreover, for all  $s_1, s'_1 \in S(M_1)$ ,  $s_2, s'_2 \in S(M_2)$ , and  $a \in \Sigma(M_1 \parallel M_2)$ , the labeling function  $\mathcal{L}(M_1 \parallel M_2)$  and the transition relation  $T(M_1 \parallel M_2)$  are defined as follows:

- $\mathcal{L}(M_1 \parallel M_2)((s_1, s_2)) = \mathcal{L}(M_1)(s_1) \cup \mathcal{L}(M_2)(s_2)$ .
- If  $s_1 \xrightarrow{a} s'_1$  and  $s_2 \xrightarrow{a} s'_2$  then  $(s_1, s_2) \xrightarrow{a} (s'_1, s'_2)$ .
- If  $s_1 \xrightarrow{a} s'_1$  and  $a \notin \Sigma(M_2)$  then  $(s_1, s_2) \xrightarrow{a} (s'_1, s_2)$ .
- If  $s_2 \xrightarrow{a} s'_2$  and  $a \notin \Sigma(M_1)$  then  $(s_1, s_2) \xrightarrow{a} (s_1, s'_2)$ .

This notion of parallel composition is derived from CSP [16, 27]; it is commutative and associative, so that no parentheses are needed when composing more than two LKSs together.

**Definition 3 (Simulation).** Let  $M_1$  and  $M_2$  be LKSs with  $\Sigma(M_1) = \Sigma(M_2) = \Sigma$ , and  $AP(M_2) = AP(M_1)$ . A relation  $R \subseteq S(M_1) \times S(M_2)$  is said to be a simulation relation iff it satisfies the following conditions:

1. If  $(s_1, s_2) \in R$  then  $\mathcal{L}(M_1)(s_1) = \mathcal{L}(M_2)(s_2)$ .
2. For any  $s_1, s'_1 \in S(M_1)$ ,  $s_2 \in S(M_2)$ , and  $a \in \Sigma$ , if  $(s_1, s_2) \in R$  and  $s_1 \xrightarrow{a} s'_1$  then there exists  $s'_2 \in S(M_2)$  such that  $s_2 \xrightarrow{a} s'_2$  and  $(s'_1, s'_2) \in R$ .

For two LKSs  $M_1$  and  $M_2$ , if there exists a simulation relation  $R$  such that  $(init(M_1), init(M_2)) \in R$  then we say that  $M_1$  is simulated by  $M_2$  and denote this by  $M_1 \leq M_2$ . The following is well-known [23]:

**Theorem 1.** Let  $M_1, \dots, M_n, N_1, \dots, N_n$  be LKSs such that  $N_i \leq M_i$  for  $1 \leq i \leq n$ . Then  $(N_1 \parallel \dots \parallel N_n) \leq (M_1 \parallel \dots \parallel M_n)$ .

In our framework, (existential) abstractions are obtained by ‘lumping’ together states of a concrete LKSs: abstract states are disjoint sets of concrete states; cf. [9]. In the remainder of this paper, we often use the letter  $M$  to denote a concrete LKS and its hatted counterpart  $\widehat{M}$  to denote an abstract LKS. Note that an abstraction  $\widehat{M}$  of  $M$  is entirely determined by an equivalence relation  $R \subseteq S(M) \times S(M)$ . We only consider *admissible* equivalence relations, i.e., we require that for all  $s, s' \in S(M)$ , whenever  $(s, s') \in R$  then  $\mathcal{L}(M)(s) = \mathcal{L}(M)(s')$ . Given a state  $s \in S(M)$ , we denote its corresponding equivalence class by  $[s]^R$  (or simply  $[s]$  when  $R$  is clear from context.)

**Definition 4 (Abstraction).** Let  $M$  be an LKS and  $R$  be an admissible equivalence relation on  $S(M)$ . Then  $M^R$  is the abstract quotient LKS induced by  $R$  such that (i)  $S(M^R) = \{[s] \mid s \in S(M)\}$ , (ii)  $\text{init}(M^R) = [\text{init}(M)]$ , (iii)  $AP(M^R) = AP(M)$ , (iv) for all  $[s] \in S(M^R)$ ,  $C(M^R)([s]) = C(M)(s)$  (well-defined since  $R$  is admissible), (v)  $E(M^R) = E(M)$ , and (vi)  $T(M^R) = \{([\ast], a, [\ast]) \mid (s, a, s') \in T(M)\}$ .

For  $s \in S(M)$  and  $a \in S(M)$ , the set of *abstract successors* of  $s$  along  $a$  is defined *tote*  $\text{AbsSucc}(s, a) = \{[s'] \in M^R \mid (s, a, s') \in T(M)\}$ .

It is easy to see that for any  $M$  and  $R$ ,  $M \wedge M^R$ . Combining this with Theorem 1 we get the following result.

**Lemma 1.** Let  $M_1, \dots, M_n$  be LKSs and  $R_1, \dots, R_n$  be equivalence relations. Then  $(M_1 \parallel \dots \parallel M_n) \wedge (M_1^{R_1} \parallel \dots \parallel M_n^{R_n})$ .

## 4 The Logic SE-Ai?

Following [11], we define a universal branching-time logic called *State-Event Universal Logic* (SE-Ai?). The logic is interpreted over LKSs and can be used to specify properties involving both data and actions in a natural manner. SE-Ai? is defined in negation normal form, i.e., negations are only applied to atomic propositions. Unlike ACTL or ACTL\*, it does not have a fixed set of operators. Rather, any  $\wedge$ -regular language can serve as a temporal operator. Since the logic is universal, every such operator is preceded by a universal path quantifier  $A$ .

Similarly to usual temporal operators, the new operators are applied to other formulas in the logic. Syntactically, this is done by defining an  $\omega$ -regular language  $O$  over a set of markers that serve as placeholders for the formulas to which  $O$  is applied. Since SE-Ai? is aimed at specifying both actions and data, its operators can be applied to subsets of actions as well as formulas over atomic propositions.

Formally, let  $\text{Mark} = \{m_1, m_2, \dots\}$  be a denumerable set of *markers* and let  $\bar{a} = \{m_1, \dots, m_n\}$  be a finite subset of  $\text{Mark}$ . Let  $O$  be an  $u$ -regular language over the alphabet  $2^{\bar{a}}$ . The corresponding  $n$ -ary temporal operator will be denoted by  $O$ . Let  $AP$  be a set of atomic propositions and  $U$  be a set of actions. Then the syntax of SE-Ai? is defined inductively as follows.

- If  $p \in AP$  then  $p$  and  $\neg p$  are formulas.
- If  $\phi$  and  $\psi$  are formulas then so are  $\phi \vee \psi$  and  $\phi \wedge \psi$ .
- Let  $O$  be an  $n$ -ary temporal operator and for  $1 \leq i \leq n$ ,  $\phi_i$  be either a formula or a subset of  $U$ . Then  $A O(\phi_1, \dots, \phi_n)$  is a formula.

The semantics of SE-Ai? is defined over LKSs. More precisely, given an SE-Ai? formula  $\phi$ , an LKS  $M$ , and  $s \in S(M)$  we write  $M, s \models \phi$  to mean that  $s$  satisfies  $\phi$ , defined inductively as follows:

- For  $p \in AP$ ,  $M, s \models p$  iff  $p \in C(s)$  and  $M, s \models \neg p$  iff  $p \notin C(s)$ .
- $M, s \models (\phi_1 \vee \phi_2)$  iff  $M, s \models \phi_1$  or  $M, s \models \phi_2$ .
- $M, s \models \phi_1 \wedge \phi_2$  iff  $M, s \models \phi_1$  and  $M, s \models \phi_2$ .

-  $M, s \models \text{AO}(\langle p_1, \dots, V_n \rangle)$  iff for every path  $TT$  starting from  $s$ , we have  $M, TX \models \mathbf{O}(\varphi_1, \dots, \langle p_n \rangle)$  [as defined below].

Let  $IT = s \xrightarrow{a} s_1 \xrightarrow{a} s_2 \dots$  be a path of  $M$  and  $7T^i$  be its suffix starting from  $S_i$ . We first define when  $TT$  satisfies an argument  $\langle \mathcal{R} \rangle$  of the operator  $\mathbf{O}$ .  $M, ?r \models ipk$  iff either  $\langle \mathcal{R} \rangle \subseteq E$  and  $\text{ao } G \text{ if } k$ , or  $\langle \mathcal{R} \rangle$  is a formula and  $M, s_0 \models (fk-$

Let  $\mathbf{O}(\langle ?i, \dots, \langle f_n \rangle \rangle)$  be as above, and  $\mathcal{O}$  be the  $\wedge$ -regular language corresponding to  $\mathbf{O}$ . Recall that the alphabet of  $\mathcal{O}$  is  $2^{\overline{M}}$  where  $\overline{M} = \{m_1, \dots, m_n\}$ . Then  $M, TT \models \mathbf{O}(\langle ?i, \dots, \langle f_n \rangle \rangle)$  iff there is a word  $o = o_1 o_2 \dots o_n \in \mathcal{O}$  such that for all  $i \geq 0$  and for all  $m_i \in G \circ \rangle, M, TT^i \models (p_i$ .

Lastly, we write  $M \models ip$  to mean  $M, \text{init}(M) \models (p$ .

As an example, let  $\mathcal{O} = \{m_1, m_2\}^* \{m_1, m_3\} \{m_1\}^* \{j\}^\wedge$  be an  $\text{cj}$ -regular expression. Then  $\mathbf{O}(\langle ?\{a\}, \{b\}, \text{if } r \rangle)$  represents an 'until' operator that captures paths in which  $(pTJip$  holds along a sequence of actions ending with the action  $b$ . This example demonstrates that in addition to formulas  $ipk$  that should hold, the logic  $\text{SE-Ai?}$  allows us to restrict the actions that can be performed, by using  $ipk \subseteq E$ .

An important property of the logic  $\text{SE-Ai?}$  is that it is preserved by the simulation relation. This is formalized by the following lemma.

**Lemma 2.** *Given two LKSs  $M_1$  and  $M_2$  and an  $\text{SE-Ai?}$  formula  $ip$ , if  $M_2 \models \langle p$  and  $M_1 \wedge M_2$ , then  $M_1 \models (p$ .*

## 5 Compositional CEGAR Verification for $\text{SE-AJ?}$

Let  $M_1, \dots, M_n$  be LKSs and let  $(p$  be an  $\text{SE-Ai?}$  formula. In seeking to determine whether  $M = M_1 \parallel \dots \parallel M_n \models ip$ , we wish to avoid constructing the full LKS  $M$ , since the size of its state space increases exponentially with the number of its components. We therefore first compute a (typically much smaller) abstraction  $\widehat{M}_i$  of each component  $M_i$ , and only then check whether  $\widehat{M} = \widehat{M}_1 \parallel \dots \parallel \widehat{M}_n \models (p$ . If this holds, we conclude that  $M \models ip$  as well. Otherwise, we extract from  $\widehat{M}$  a counterexample  $\widehat{C}$  violating  $ip$ , and check whether this counterexample is valid, i.e., whether it corresponds to a real execution of  $M$ . In the affirmative, we conclude that  $M \wedge ip$ . Otherwise, we use this spurious counterexample to refine our abstractions, and repeat the process until either a real counterexample is found or the property is shown to hold. The main strength of our approach is the fact that the abstraction, counterexample-validation, and refinement steps are all carried out one component at a time, so that it is never necessary to construct the full state space of the concrete system  $M$ .

### 5.1 Model Checking

Let  $\widehat{M}$  be an LKS<sup>1</sup>,  $s \in S(\widehat{M})$ , and  $tp$  be an  $\text{SE-Ai?}$  formula. We give a model-checking algorithm to determine whether  $\widehat{M}, s \models tp$ . We proceed by structural induction on  $ip$ ,

<sup>1</sup> In the interests of consistency and clarity, we present our approach in both this section and the next in terms of the abstract LKS  $\widehat{M}$ , although it naturally applies to concrete systems as well.

starting with the case in which  $\varphi$  is of the form  $\mathbf{AO}(\varphi_1, \dots, \varphi_n)$ . Let  $O$  be the  $\omega$ -regular language over  $\bar{m} = \{m_1, \dots, m_n\}$  corresponding to  $O$ . The algorithm consists of the following steps: (i) compute from  $\widehat{M}$  and  $s$  the ‘smallest’  $\omega$ -regular language  $O_s$  over the alphabet  $2^{\bar{m}}$  such that  $\widehat{M}, s \models \mathbf{AO}_s(\varphi_1, \dots, \varphi_n)$ , and (ii) check whether  $O_s$  is ‘subsumed’ by  $O$ .

Intuitively, the idea is to interpret each path  $\pi$  in  $\widehat{M}$  as a sequence of maximal subsets of formulas (among  $\varphi_1, \dots, \varphi_n$ ) that hold along  $\pi$ . We then check whether replacing each  $\varphi_j$  with the corresponding marker  $m_j$  results in a sequence belonging to  $O$ .

In order to do so we build an automaton  $B_s$  obtained from  $\widehat{M}$  by replacing every action  $a$ , in transitions of the form  $(q, a, q')$ , with the subset of markers corresponding to the formulas that hold for the transition. More precisely, if  $\varphi_j$  is an SE-A $\Omega$  formula, we include the corresponding marker  $m_j$  provided that  $\widehat{M}, q \models \varphi_j$ , and if  $\varphi_j \subseteq \Sigma(\widehat{M})$ , we include  $m_j$  if  $a \in \varphi_j$ .

To make this more rigorous, we first recall the notion of *Büchi automata*:

**Definition 5 (Büchi Automaton).** A Büchi automaton is a 5-tuple  $B = (S, I, \Sigma, T, Acc)$  where (i)  $S$  is a finite non-empty set of states, (ii)  $I \subseteq S$  is a set of initial states, (iii)  $\Sigma$  is a finite alphabet, (iv)  $T \subseteq S \times \Sigma \times S$  is a transition relation, and (v)  $Acc \subseteq S$  is a set of accepting states.

A path of  $B$  is an infinite sequence  $\pi = q_0 \xrightarrow{a_0} q_1 \xrightarrow{a_1} \dots$  such that  $q_0 \in I$ , and for every  $i$ ,  $(q_i, a_i, q_{i+1}) \in T$ .  $\pi$  is accepting if it visits the set  $Acc$  infinitely often.

The language  $O_s$  is represented by a Büchi automaton  $B_s$ , which is derived from  $\widehat{M}$  as follows:  $B_s = (S_s, I_s, \Sigma_s, T_s, Acc_s)$ , where (i)  $S_s = S(\widehat{M})$ , (ii)  $I_s = \{s\}$ , (iii)  $\Sigma_s = 2^{\bar{m}}$ , (iv)  $Acc_s = S(\widehat{M})$ , and (v)  $T_s$  is the set of transitions such that for each  $(q, a, q') \in T(\widehat{M})$ ,  $T_s$  includes a transition  $(q, \bar{m}', q')$  such that  $\bar{m}' \subseteq \bar{m}$  and the following condition holds: for  $0 \leq j \leq n$ ,  $m_j \in \bar{m}'$  iff either  $\varphi_j \subseteq \Sigma(\widehat{M})$  and  $a \in \varphi_j$  or  $\varphi_j$  is a formula and  $\widehat{M}, q \models \varphi_j$ .

Note that in order to construct  $B_s$  we need to know whether  $\widehat{M}, q \models \varphi_i$  for every  $q \in S(\widehat{M})$  and every  $i \in \{1, \dots, n\}$ . This is achieved by invoking the model checking algorithm recursively.

In the second step, we must check whether  $O_s$  is *subsumed* by  $O$ . Observe first that it is not enough to simply check whether  $O_s \subseteq O$ . That is because  $O$  and  $O_s$  are defined over the alphabet  $2^{\bar{m}}$ , and SE-A $\Omega$  is ‘monotonic’ (cf. [11]). In order to define monotonicity of SE-A $\Omega$  we consider two  $\omega$ -regular languages  $O$  and  $O'$  over  $\bar{m}$  that satisfy: for every  $w = w_1 w_2 \dots \in O$  there exists  $w' = w'_1 w'_2 \dots \in O'$  such that for every  $i \geq 1$ ,  $w_i \subseteq w'_i$ . Then for every model  $\widehat{M}$ , if  $M \models \mathbf{AO}'(\varphi_1, \dots, \varphi_k)$  then  $M \models \mathbf{AO}(\varphi_1, \dots, \varphi_k)$ . For example, let  $\bar{m} = \{m_1, m_2, m_3\}$ , and suppose that  $O = \{m_2\}^\omega$  and that  $O_s = \{m_1, m_2\}^\omega$ . Then  $\widehat{M}, s \models \mathbf{AO}_s(\varphi_1, \varphi_2, \varphi_3)$  and, thanks to monotonicity,  $\widehat{M}, s \models \mathbf{AO}(\varphi_1, \varphi_2, \varphi_3)$  as well, even though  $O_s \not\subseteq O$ . To overcome this problem, we check whether  $O_s \subseteq \uparrow O$ , where  $\uparrow O = (\{m_2\} + \{m_1, m_2\} + \{m_2, m_3\} + \{m_1, m_2, m_3\})^\omega$ . The language  $\uparrow O$  is called the *monotonic closure* of  $O$  and, intuitively, is obtained by replacing in  $O$  every occurrence of a set of markers  $\bar{m}' \subseteq \bar{m}$  by the sum of all the sets of markers  $\bar{m}''$  such that  $\bar{m}' \subseteq \bar{m}'' \subseteq \bar{m}$ . Formally:

**Definition6 (Monotonic Closure).** Let  $B = (S_B, IB, 2^W, T_B, ACC_B)$  be a Büchi automaton accepting some  $uj$ -regular language  $O$ . The monotonic closure of  $O$  is the  $oo$ -regular language  $\hat{O}$  accepted by the Büchi automaton  $IB = (\mathbb{E}te, /f\mathbb{E}, 2^{\overline{m}}, T^B, ACC^B)$  constructed from  $B$  as follows:  $S^B = SB$ ,  $I^B = IB$ ,  $Acc_w = Acc_B$ , and  $T_{IB} = \{(q, m^?, q') \mid \exists \overline{r} \in C_{\overline{m}^?} \cdot (q, \overline{m}^?, q') \in T_B\}$ .

The correctness of our two-step procedure is encapsulated by the following:

**Theorem 2.**  $\widehat{M}, s \models AOfai, \dots, ip_n$  iff  $O, \underline{C} \models tO$ .

The other cases (in which  $\langle p$  is not an  $cj$ -regular operator) are straightforward. To summarize,  $\widehat{M}, s \models v?$  iff:

- $pG \mathbb{E}(5)$  if  $(p = p$  and  $p \neq \mathbb{E}(5)$  if  $\langle p = \neg p$ , where  $p \in J4P$ .
- $\widehat{M}, s \models ifi$  and  $\widehat{M}, s \models y?$  if  $\langle p = \langle fi \wedge \mathbb{E}2$
- $\widehat{M}, s \models ifi$  or  $\widehat{M}, s \models (??$  if  $\langle P = \wedge 1 \vee y?$
- $\sim O_s \underline{C} \wedge 0$  if  $(f = AO((\wedge i, \dots, \langle \wedge_n)$ , where  $O_s$  and  $fO$  are defined as above.

## 5.2 Counterexample Generation

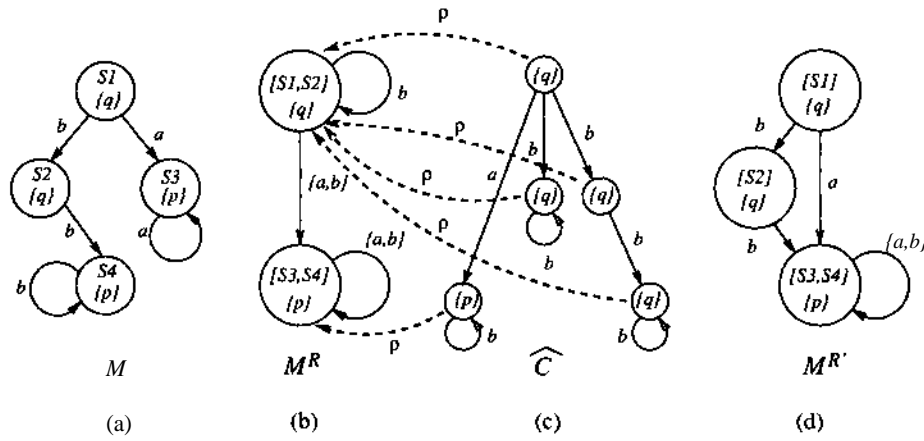
Let  $\widehat{M}$  be an LKS,  $s \in S(\widehat{M})$ , and  $ip$  be an SE-Ai7 formula. Suppose that  $\widehat{M}, s \wedge (p$ . In this section, we show how to compute a counterexample to  $ip$ , i.e., a fragment of  $\widehat{M}$  beginning at state  $s$  that violates  $ip$ . As for the model-checking algorithm of SE-A4?, we give a recursive procedure:

- If  $ip = (i) \vee \wedge$ , then compute counterexamples  $\widehat{C}_1$  and  $\widehat{C}_2$  to  $(pi$  and  $\langle 2$  respectively, and glue  $\widehat{C}_1$  and  $\widehat{C}_2$  at their initial states. Indeed,  $\widehat{M}, s \wedge 1 \vee \wedge$  iff  $\widehat{M}, s \wedge (\widehat{C}_1 \text{ and } \widehat{C}_2)$ .
- If  $\langle \wedge = ip_x \wedge (??$  then compute a counterexample either to  $(\wedge i$  or to  $p2$ . Indeed,  $\widehat{M}, s \wedge Y? \langle Pi \wedge \wedge 2$  iff  $\widehat{M}, s \wedge \wedge 1$  or  $\widehat{M}, s \wedge \langle \wedge_2$ .
- If  $\langle ? = AO((/?i, \dots, v?_n)$  proceed as follows. Since  $\widehat{M}, s \wedge = (p$ , there exists a pattern in  $O_s$  that is not in  $tO$ . Let  $\overline{r} = s_0 \overline{r} \cdot Si \overline{r} \dots$  (where  $s_0 = s$ ) be an accepting path of  $B_s$  such that the  $cj$ -word  $ffToTfii \dots$  does not belong to  $tO$ . Recall that by the definition of the automaton  $B_s$ , each transition  $Si \xrightarrow{\overline{m}^?} s'$  in  $T_{B_s}$  corresponds to a transition  $si \xrightarrow{\wedge} s'$  in  $T(\widehat{M})$ . Let therefore  $so \xrightarrow{\wedge} \langle i \xrightarrow{\wedge} \dots$  be the corresponding path of  $\overline{r}$  in  $\widehat{M}$ . This path then clearly violates  $O(fi, \dots, y?_n)$ . To compute a counterexample to  $(?$ , it suffices to take this path and to glue to each state  $Si$  counterexamples to all formulas  $ipj$  such that  $\widehat{M}, Si \wedge ipj$ . (Note that, while the path is infinite, it comprises of only finitely many distinct states.)

Owing to the direct manner in which a counterexample  $\widehat{C}$  is extracted from an LKS  $\widehat{M}$ , there is a canonical mapping  $p : S(\widehat{C}) \gg S(\widehat{M})$  which satisfies the following conditions: (i)  $p(\text{init}(\widehat{C})) = \text{init}(\widehat{M})$ , (ii) for all  $q \in S(\widehat{C})$ ,  $C(\widehat{C})(q) = \widehat{C}(\widehat{M})(p(q))$ , and (iii) if  $(q, a, q') \in T(\widehat{C})$ , then  $(p(q), a, p(q')) \in T(\widehat{M})$ . We shall make use of  $p$  later on in the refinement step.



*Example 1.* Figure 1 (a) shows an LKS  $M$  with  $AP(M) = \{p, q\}$ ,  $E(M) = \{a, b\}$ , and initial state  $s_1$ . (b) shows the abstract quotient LKS  $M^R$  induced by the equivalence relation  $R$  having equivalence classes  $\{s_1, s_2\}$  and  $\{s_3, s_4\}$ . Let  $tp$  be the formula (in CTL\*-like notation)  $AG(\{o\} \Rightarrow A(p \vee Xp \vee XXp))$ .  $ip$  asserts that on all paths, whenever the action  $a$  occurs from a state  $s$ , then the atomic proposition  $p$  either holds at  $s$  or, along any path starting at  $s$ , in one of the next two states. It is not hard to see that  $M^R \not\models ip$  and indeed (c) shows a counterexample  $\widehat{C}$  illustrating this. The dotted arrows from  $\widehat{C}$  to  $M^R$  represent the canonical mapping  $p$ .



**Fig.1.** (a) concrete LKS  $M$ ; (b) Abstract LKS  $M^R$ ; (c) counterexample  $\widehat{C}$ ; (d) refined abstract LKS  $M^{R>}$ .

Observe, however, that the counterexample is in fact spurious. Indeed, the abstract LKS  $M^{R>}$  pictured in (d) is a refinement of  $M^R$  induced by the equivalence relation  $R'$  having equivalence classes  $\{s_1\}$ ,  $\{s_2\}$ , and  $\{s_3, s_4\}$ . Since  $M^{R>} \models ip$ , we conclude that  $M \models ip$  as well.

### 5.3 Counterexample Validation

Suppose that  $\widehat{M}, s \wedge tp$  for some SE-AJ? formula  $tp$ , and let  $\widehat{C}$  be a counterexample to  $(p)$ . Recall that  $\widehat{M}$  is an abstraction of a concrete LKS  $M$ . We say that  $\widehat{C}$  is a *valid* counterexample iff  $\widehat{C} \wedge M$ . Indeed, from Lemma 2 we get:

**Theorem 3.** *Let  $(p)$  be an SE-Ai? formula. If  $\widehat{C} \wedge M$  and  $C \models ip$ , then  $M \models ip$ .*

Intuitively, this holds because SE-Ai? formulas describe properties that are quantified over all possible paths of the structure.

This result suggests a way to formally check whether a counterexample  $\widehat{C}$  is valid for a concrete system  $M$  or not. However, as mentioned earlier, when  $M$  is a concurrent C program built of components  $M_1, \dots, M_n$ , we are faced with the problem that even

if each component  $M_i$  has a finite state space, constructing the state space of  $M$  might be prohibitive in practice due to exponential blowup. To overcome this problem, we propose to check if the concrete system  $M$  simulates the counterexample  $\hat{C}$  in a compositional way by checking whether for every  $i \in \{1, \dots, n\}$ ,  $M_i$  weakly simulates the  $i^{\text{th}}$  projection of  $\hat{C}$ .

**Definition 7 ( $i^{\text{th}}$  Projection).** Let  $M = M_1 \parallel \dots \parallel M_n$  be a parallel composition of LKSs, and let  $\hat{C}$  be a further LKS. For any  $i \in \{1, \dots, n\}$ ,  $\hat{C}U$  is the LKS defined by: (i)  $S(\hat{C}U) = S(\hat{C})$ , (ii)  $\text{init}(\hat{C}U) = \text{init}(\hat{C})$ , (iii)  $AP(\hat{C}U) = AP(\hat{C})$ , (iv) for any  $s \in S(\hat{C}U)$ ,  $C(\hat{C}U)(s) = C(\hat{C})(s) \cup C(M_i)$ , (v)  $S(\hat{C}U) = S(\hat{C}) \cup \{r\}$ <sup>2</sup>, and (vi)  $T(\hat{C}U)$  is defined as follows:

$$\begin{aligned} & \neg \exists (s, a, s') \in T(\hat{C}) \text{ and } a \in E(M_i) \text{ then } (s, a, s') \in T(\hat{C}U). \\ & \neg \exists (s, a, s') \in T(\hat{C}) \text{ and } a \in S(M_i) \text{ then } (s, r, s') \in T(\hat{C}U). \end{aligned}$$

The introduction of  $r$  actions also naturally leads to a weak version of simulation, which we define next specialized to the case in which only the system being simulated is capable of performing  $r$ 's.

**Definition 8 (Weak Simulation).** Let  $\hat{C}$  and  $M$  be LKSs such that  $E(\hat{C}) = E(M) \cup \{r\}$  and  $AP(\hat{C}) = AP(M)$ . A relation  $R \subseteq S(\hat{C}) \times S(M)$  is said to be a weak simulation relation iff  $R$  satisfies the following conditions:

1. If  $(s_1, s_2) \in R$  then  $C(\hat{C})(s_1) = C(M)(s_2)$ .
2. For any  $s, s' \in S(\hat{C})$ ,  $s_2 \in S(M)$ , and  $a \in E(\hat{C}) \setminus \{r\}$ , if  $(s_1, s_2) \in R$  and  $s \xrightarrow{a} s'$  then there exists  $s'_2 \in S(M)$  such that  $s_2 \xrightarrow{a} s'_2$  and  $(s', s'_2) \in R$ .
3. For any  $s_1, s_2 \in S(\hat{C})$  and  $s_2 \in S(M)$ , if  $(s_1, s_2) \in R$  and  $s_1 \xrightarrow{r} s'_1$  then  $(s'_1, s_2) \in R$ .

For two LKSs  $\hat{C}$  and  $M$ , if there exists a weak simulation relation  $R$  such that  $(\text{init}(\hat{C}), \text{init}(M)) \in R$  then we say that  $\hat{C}$  is weakly simulated by  $M$  and denote this by  $\hat{C} \preceq M$ .

The following key result forms the basis of our compositional approach to counterexample validation.

**Theorem 4 (Compositionality).** Let  $M_1, \dots, M_n$  be LKSs and let  $\hat{C}$  be a further LKS. Then  $\hat{C} \preceq (M_1 \parallel \dots \parallel M_n)$  iff  $\hat{C}U \preceq M_i$  for  $1 \leq i \leq n$ .

*Proof (Sketch.)* Consider the case  $n = 2$ ; the general case is handled in a similar manner. Suppose first that  $\hat{C} \preceq M_1 \parallel M_2$ . Let  $R \subseteq S(\hat{C}) \times S(M_1 \parallel M_2)$  be a corresponding simulation relation. Define  $R_1 = \{(s, s_1) \mid \exists s_2. (s, (s_1, s_2)) \in R\}$ , and  $R_2 = \{(s, s_2) \mid \exists s_1. (s, (s_1, s_2)) \in R\}$ . It is readily verified that  $R_1$  (resp.  $R_2$ ) is a weak simulation relation between  $\hat{C}|_1$  and  $M_1$  (resp.  $\hat{C}|_2$  and  $M_2$ ). Therefore  $\hat{C}|_1 \preceq M_1$  and  $\hat{C}|_2 \preceq M_2$ .

<sup>2</sup> We assume that  $r$  is a fresh action not otherwise present in the alphabet of LKSs.

In the other direction, let  $R_1$  and  $R_2$  be two weak simulation relations witnessing  $\widehat{C}_1 \wedge M_1$  and  $\widehat{C}_2 \preceq M_2$  respectively. Let  $R = \{(s, si) \mid (s, si) \wedge i \in A(5,52) \wedge R_2\}$ . It is easy to check that  $R$  is a simulation relation between  $\widehat{C}$  and  $M \parallel M_2$ , as required. •

Putting everything together, we get:

**Corollary 1.** *Let  $M_1, \dots, M_n$  be LKSs,  $\varphi$  an SE-AQ formula, and  $\widehat{C}$  an abstract counterexample to  $M \parallel \dots \parallel M_n \models \varphi$ . Then  $\widehat{C}$  is a valid counterexample iff  $\widehat{C}_i \not\preceq M_i$  for every  $i \in \{1, \dots, n\}$ .*

Checking whether  $\widehat{C}_i \not\preceq M_i$  is done in a standard manner by a fixpoint computation of the maximal weak simulation relation between  $\widehat{C}_i$  and  $M_i$ .

#### 5.4 Abstraction Refinement

We now describe our counterexample-guided refinement procedure. Suppose that  $\widehat{C} \wedge M$ ; then the counterexample  $\widehat{C}$  is spurious, and we need to refine our abstraction  $\widehat{M} = \widehat{M}_1 \parallel \dots \parallel \widehat{M}_n$ . We achieve this by examining each of the abstractions  $\widehat{M}_i$  individually: for  $i \in \{1, \dots, n\}$ , we refine  $\widehat{M}_i$  if  $\widehat{C}_i \not\preceq M_i^*$ . To this end, fix  $j$  an index in  $\{1, \dots, n\}$  such that  $\widehat{C}_j \wedge M_j$ . Recall that  $\widehat{M}_j$  is a quotient LKS of the form  $M_j^{R_j}$ , where  $R_j$  is an equivalence relation on  $S(M_j)$ . Our refinement step consists in producing a strictly finer equivalence relation than  $R_j$ .

Recall the canonical mapping  $p : S(\widehat{C}) \rightarrow S(\widehat{M})$  defined in Section 5.2, and let  $p_j : S(\widehat{C}_j) \rightarrow S(\widehat{M}_j)$  be its corresponding  $j$ 'th projection. We can show that:

**Lemma 3.** *Suppose that for any  $s \in S(\widehat{C}_j)$ , any  $a \in \text{Enabled}(s)$ , and any  $s_1, s_2 \in p_j(s)$ , we have that  $\text{AbsSucc}(s, a) = \text{AbsSucc}(s_1, a)$ . Then  $\widehat{C}_j \not\preceq M_j$ .*

Since, by assumption,  $\widehat{C}_j \wedge M_j$ , it follows from Lemma 3 that there exist a state  $s \in S(\widehat{C}_j)$ , an action  $a \in \text{Enabled}(s)$ , and two states  $s_1, s_2 \in p_j(s)$  such that  $\text{AbsSucc}(s, a) \wedge \text{AbsSucc}(s_1, a)$ . Let  $R_j'$  be a new equivalence relation derived from  $R_j$  by sub-partitioning the equivalence class  $\widehat{C}_j$  as follows:  $q, q'$  belong to the same sub-partition iff  $\text{AbsSucc}(q, a) = \text{AbsSucc}(q', a)$ .  $R_j'$  is clearly a proper refinement of  $R_j$  (i.e. the number of  $R_j'$  will be strictly greater than that of  $R_j$ ), and is moreover admissible since  $R_j$  was admissible. It should be noted that the refined abstract LKS  $M_j^{R_j'}$  is however *not* guaranteed to refute the (projected) counterexample  $\widehat{C}_j$ . For example, Figure 1 shows the abstract LKS  $M^R$  and its refinement  $M^{R'}$  which, in this case, refutes the spurious counterexample  $\widehat{C}$ .

Since the refinement procedure always yields a proper refinement and since each LKS is finite, the CEGAR-based SE-AI? verification algorithm always terminates. In particular, spurious counterexamples are always eventually refuted.

## 6 Applications and Future Work

We implemented our compositional approach for verification of branching-time logics in the MAGIC tool, developed at Carnegie Mellon [5, 22]. MAGIC extracts finite LKS models from C programs. We applied the SE-Ai? model checking compositional loop for verification of a set of benchmarks whose abstract models were automatically extracted by MAGIC. We verified code provided by our industrial partner, one of the market leading robot manufacturers worldwide. We analyzed the IPC (InterProcess Communication) protocol used to mediate communication in a multi-threaded robot controller software. We model checked the synchronous communication portion of the IPC protocol which was implemented in terms of messages passed between queues owned by different threads. We specified a set of more than 20 SE-AJ? properties most of which were expressed using both states and events. That was required to make proper assertions on the communication actions carrying data.

We found a **bug** in the provided version of the IPC code and reported it to our industrial partner. The bug was a race condition in which a writer mistakenly blocks while trying to write to a queue that is not full. That bug violated the property that no communications timeout when they could be safely delivered. It had been undetected despite seven years of industrial use of the IPC, including a substantial testing phase.

We are currently examining other case studies. For future work, we would also like to carry out a systematic evaluation of the expressiveness of the SE-Ai? logic in comparison to other universal logics, estimating the complexity of our algorithms and improving the methods presented in this paper.

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